

# The High Speed Digital Symposium takes place on Thursday, October 18th in Ballroom H



**1:20-1:50 PM**

**Linear Voltage Regulator Model (VRM) for Power Integrity Simulations**

**2:00-2:30 PM**

**Power Distribution Network (PDN) Impedance and Target Impedance"**

**Speaker:**

Larry Smith,  
PDN Power Integrity



**2:40-3:10 PM**

**100uΩ Probing Methods**

**Speaker:**

Brian Hostetler,  
Cray, Inc.



**3:20-3:50 PM**

**Designing and Measuring 100uOhm Power Rails**

**Speaker:**

Steve Sandler,  
Picotest

**4:00-4:30 PM**  
**EXPERT PANEL**  
**Putting Power Integrity in Perspective: How do we know when to apply what guidelines to what types of systems.**

## HIGH SPEED DIGITAL SYMPOSIUM

The symposium consists of four presentations followed by a panel discussion with each of the speakers participating, led by the editor of Signal Integrity Journal, Eric Bogatin.

**REGISTER NOW**

For more information and to learn about each panel discussion, please visit: <http://www.ediconusa.com/digitalsymposium.asp>



**Symposium Moderator:**

Eric Bogatin,  
Signal Integrity Journal Editor &  
Signal Integrity Evangelist with Teledyne LeCroy

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